

Figure 1: PCB layout of the proposed 7-bit DAC. The layout shows a central array of 7 DAC cells (labeled 1-7) and a ground plane (labeled 7&GND). Dimensions include a total width of 15.90 mm, a total height of 11.85 mm, and various component spacings and offsets.

RECOMMENDED PCB LAYOUT

1.MATERIAL:

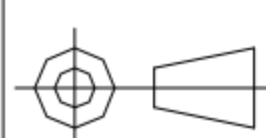
- ### 3.SPECIFICATION:

- 3.1 Rate: 3.0 A
- 3.2 insulator Resistance: 1000M Ω Min
- 3.3 Dielectric Strength: 500V AC
- 3.4 Contact Resistance: 30m Ω Max
- 3.5 Operation Temperature: -25°C ~ +85°C
- 3.6 Insertion Force: 35 N
- 3.7 Extraction Force: 10 N

 东莞市溪榜电子有限公司
Switch Connector DONG GUAN XI BANG ELECTRONICS CO., LTD

TOLERANCE UNLESS OTHERWISE STATED :	Up to 5	± 0.2
	Above 5 ~ 15	± 0.3
	Above 15 ~ 30	± 0.4
	Above 30 ~ 50	± 0.5
	Angle	± 0.3°

3RD.
ANGEL'S



UNITS

MM

DRAWN BY:	DATE
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Jack Lu	05/13/2
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CHECKED BY:	DATE
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Jacky Chen	05/13/2
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APPROVED BY:	DATE
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MAT'L

FINISH

SCALE

SHEET NO.

DATE _____

05/13/21

DATE _____

05/13/21

DATE _____

TITLE

MODLE

DWG No.

PART N

CONNECTOR

USB AF 3.0 沉板 板上H2.11mm 無捲邊 GEN 2

b. SUA-110E15-32x-S242

0. SUA-110E15-32x-S242

SIZE

A4
VFD

VER
D